	MANDAT	ORY DISCLOSURE
	Mandatory Disclosure	Updated on 29/10/2024
	AICTE File No.	F. No. Western /2019-20/1-4290336542, Date: 30- Apr-2019
1	Name of the Institute	Shri. Siddheshwar Devasthan, Solapur. Shree Siddheshwar Women's College of Engineering Solapur.
	Address of the Institution	T.P.S. II, Final Plot No. 74, Bhavani Peth, Rupa Bhavani Mandir Road, Solapur, Maharashtra, 413002
	City & Pin Code	Solapur - 413 002
	State / UT	Maharashtra
	Phone number with STD code	0217-2627227
	Email	office@sswcoe.edu.in
	Website	http://www.sswcoe.edu.in/
	Nearest Railway Station (dist. in Km)	Solapur Railway Station, 3.7 Km.
	Nearest Airport (dist. In Km)	Solapur Airport, 9.7 Km, Pune 234 Km
	Type of Institution	Unaided – Private
	Category (1) of the Institution	Linguistic-Minority
	Category (2) of the Institution	Women's College
	Name of the organization running the Institution	Shri. Siddheshwar Devasthan, Solapur
	Address of the organization	Shri. Siddheshwar Devasthan, Solapur 1, Siddheshwar Peth, Shri Siddheshwar Mandir, Solapur. Maharashtra, 413001.
2	Registered with	Assistant Charity Commissioner, Poona Region. B.P.T Act 1950 No.: 249
	Registered date	05 September 1953
	Email-Id	siddheshwardevasthan@gmail.com
	Telephone No.	0217-2725051

	Name of the Principal/ Director	Dr. T. A. Chavan
	Exact Designation	Principal
	Phone number with STD code	9689934809
3	Email	principal@sswcoe.edu.in
	Highest Degree	Ph.D.
	Field of Specialization	Computer Science and Engineering(Mobile Communication)

		Dr. Babasaheb Ambedkar Technological University(DBATU), Lonere.
4	Address	A/P:Lonere, Tal. Mangaon, Dist. Raigad (M.S.)
4	Website	https://dbatu.ac.in/
	Latest affiliation period	2024-25

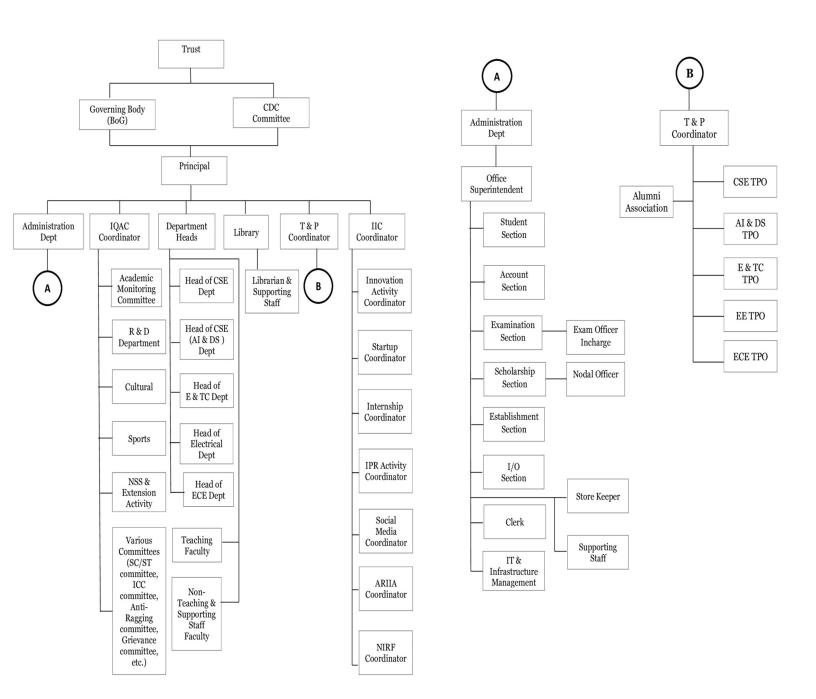
5 (a)	Members of Board of Governors				
Sr. No.	Name	Designation	Remarks		
1	Shri. Dharmraj A. Kadadi	Chairman	President, Shri Siddheshwar Devasthan, Solapur (Indstrialist & Educationist)		
2	Dr. Rajshekhar R. Yelikar	Member	Trustee		
3	Shri. Shivkumar S. Patil	Member	Trustee		
4	Prof. Channabasava B. Nadagouda	Member	Trustee & Educationist		
5	Shri. Kashinath R. Dhole	Member	Trustee & Educationist		
6	Shri. Prakash Sangappa Birajdar	Member	Trustee		
7	Shri. Pashupatinath Vijaaykumar Mashal	Member	Trustee		
8	Shri. Sharad K. Thakre	Member	Indstrialist		
9	Prof. Gajanan R. Dharane	Member	Educationist		
10	Dr. S. I. Nalbalwar, Nominee of Dr. Babasaheb Ambedkar Technological University, Lonere	Member	Nominee of Affiliating University		
11	Nominee of State Government	Member	Nominee of State Government		
12	Industrialist /Technologist/ Educationist from the Region nominated by the State Government	Member	Industrialist /Technologist/ Educationist from the Region nominated by the State Government		
13	Dr. Avinash S. Patil	Member	Faculty Member		
14	Prof. S. C. Mhamane	Member	Faculty Member		
15	Dr. Tukaram A. Chavan	Member- Secretary	Principal, SSWCOE, Solapur		

5 (b)	Members of Academic Advisory Body (College Development Committee)				
Sr. No.	Name of Members	Position Held in CDC	Designation		
1	Shri. Dharmraj A. Kadadi	Management	Chairman		
2	Shri. Nilkanthappa M. Konapure	Management	Member Secretary		
3	Prof. Channabasava B. Nadagouda	Education	Member		
4	Mr. Bharat Vedpathak	Social Services	Member		
5	Dr, Rajivkumar S. Mente	Research	Member		
6	Ms. Vaihnavi S. Katkar	Industry-Alumnus	Member		
7	Dr. Avinash S. Patil	HoD GSE Dept	Member		
8	Dr. Vijaysinh G. Chavan	Teacher	Member		
9	Dr. Somnath B. Thigale	Teacher	Member		
10	Prof. Savita. M. Gungewale	Women Teacher	Member		
11	Prof. Santoh. J. Madki	Co-ordinator IQAC	Member		
12	Mr. Dhanraj. R. Berungikar	Non-Teaching Staff	Member		
13	Ms. Kshitija G. Chakote	President, college student council	Member		
14	Ms. Anjali Gaoudgaon	Secretary, college student council	Member		
15	Dr. Tukaram A. Chavan	Head of the Institution	Member-Secretary		

5 (c) Frequency of the Board Meeting and Academic Advisory Body

Governing Body meetings are conducted 3-4 times in a year

5(d) **Organizational Chart**



5 (e) Nature and Extent of involvement of Faculty and Students in academic affairs/improvement

- Faculty and students are encouraged to publish research papers in national, international, UGC and SCOPUS Journals to inculcate the spirit of research.
- Faculty is encouraged for training in various faculty development programs to make them both multi-skilled and area specialized.
- The faculty uses innovative techniques for interactive lectures, regular lab assignments, and project work to develop and strengthen problem solving approach and creative thinking along with communication skills among students.
- To promote and foster excellence in learning skills of students, Institution has developed a specialized teaching learning pedagogy known as "ETLP", it is a special learning oriented program created for interactive and lifelong learning in our institute under the guidance of Prof. C. B. Nadagouda, SSWCOE, Solapur. ETLP takes care of student's learning. It confirms learning and understanding of subjects which are reflected through results.
- Students are encouraged to explore their hidden talent through participation in professional / curricular / co-curricular and Extra-curricular activities inside as well as outside the Institute.

The policy for promoting the staff for higher education (BE/ME/PhD):

A) FOR PURSUING Ph. D:

The Faculty pursuing Ph.D. in the institute for part time/full time where the residential course workrequirement is mandatory:

- 1. Permanent faculty members with at least three (3) years regular service after their regular appointment at SSWCOE would be eligible for sponsorship. The head of institute will recommend to BoG.
- 2. After getting the approval of BoG, the faculty who are willing to pursue Ph.D. (Internal or External), has to execute a notarized bond valuing of Rs. 3,00,000/- (Rupees Three Lakhs only) for serving the Institution for a minimum period of three years after the award of Ph.D. Degree.
- 3. The same rule is applicable to the faculty who joins this Institution while pursuing his/her Ph.D. in other Universities, provided he/she desires to utilize the facilities of our College.
- 4. The SSWCOE will give full time study leave of maximum of one semester with basic pay to complete the course work requirement at the research institute.
- 5. If the faculty leaves the Institution by breaching the bond executed by him/her, the bond value has to be remitted by the concerned faculty.
- 6. Faculty up to a limit of 25% of the total faculty strength of the Department shall be allowed to pursue higher education. For the purpose of calculation of 25%, the faculty who are already pursuing higher education shall also be taken into account. In other words, in any given date, the total no. of staff who shall pursue higher education shall not exceed 25% of the total strength of the department depending on seniority and merit.

- 7. Every sponsored faculty will submit every six (6) months progress report to the institute.
- 8. Faculty may get extension of maximum one semester with justification if required with the approval of BoG.
- 9. Three (3) increments will be given after the award of Ph.D. degree and fulfilling other requirements as per AICTE, University, State Government rules.
- 10. The BoG reserves the rights to alter, modify above rules as per the requirement of Institute.

B) FOR PURSUING U.G. /P. G.:

- 1. Permanent staff with at least three (3) years regular service at SSWCOE would be eligible for sponsorship. The head of institute will recommend to BoG.
- 2. After getting the approval of BoG, the staff pursuing UG/PG has to execute a notarized bond valuing Rs. 2,00,000/- (Rupees Two Lakhs only) for serving the Institution for a minimum period of three (3) years after awarding UG/PG degree.
- 3. If the staff leaves the Institution by breaching the bond executed by him, the bond value has to be remitted by the concerned staff.
- 4. For staff pursuing UG/PG degree, the institute will give maximum 15 days study leave per year, preferably in vacation without disturbing academic activities.
- 5. Staff up to a limit of 25% of the total staff strength of the department shall be allowed to pursue higher education. For purpose of calculation of 25%, the staff who are already pursuing higher education shall also be taken into account. In other words, in any given date, the total no. of staff who shall pursue higher education shall not exceed 25% of the total strength of the department depending on seniority and merit.
- 6. Every sponsored candidate will submit progress report after every six (6) months to institute.
- 7. The BoG reserves the rights to alter, modify above rules as per the requirement of Institute.

C) The policy for staff attending Conference /Workshop /Seminar/FDP /STTP etc. (Teacher Training Policy):

Permanent faculty members with at least two years in service after regular appointment at SSWCOE would be eligible.

- 1. Financial support for Paper Publication (for papers published in peer reviewed reputed journals)
 - i) National Journal: Rs. 3,000/- for each faculty per year.
 - ii) International Journal: Rs. 5,000/- for each faculty per year.
 - iii) Patent file: Rs. 10,000/- for each faculty per year.
 - iv) HOD will recommend for the above based on the performance of faculty.
- 2. Financial support for Conferences/Seminar/Workshop/STTP/FDP etc.:
 - i) Registration fee up to Rs. 2000/- will be paid by college and if morethan Rs. 2000/- then 50% will be paid by college.
 - ii) TA/DA (IInd class/Non-AC Bus fare) within the country will be borne byInstitute.

- iii) Maximum Rs. 7,000/- for each faculty per year
- iv) On duty leave will be given during this period.
- v) Each faculty member will be allowed only two times in the academic yearbased on the performance.

5 (f) Mechanism for Good Governance

- All stakeholders of the College- parents, alumni, employers, student, faculty and supporting staff involve in academic affairs of the College and put forward their valuable suggestions for good governance.
- Regular meetings are held between: (a) President and BoG members (b) President and CDC members (c) Principal and IQAC members (d) Principal and IIC members (e) Principal and HoDs (f) HoDs and faculty (g) Principal and Student Council (h) Parents meet (i) Alumni meet (j) Mentor (Faculty) & Students to discuss about their problems for solutions and to maintain good academics and healthier work atmosphere in College.
- Various committees are having student's representatives from each class so that student's views can be taken into consideration for betterment.
- The Principal collects class-wise feedback from students to evaluate teaching quality. Based on this feedback, the Principal holds meetings to discuss the findings with the faculty.
- The Principal holds meetings with Heads of Departments to discuss syllabus coverage, result analysis, class tests, and the need for makeup classes to ensure syllabus completion. The Principal also requests HODs to implement remedial measures as needed.
- Under training and placement, we conduct various placement activities to support student career development. These activities include organizing placement drives, coordinating with industry partners, and providing career counseling. Regular workshops, resume-building sessions, and interview preparation efforts to ensure students are well prepared for the job market.
- Various activities including technical and religious functions are celebrated with active participation of staff and students and they are encouraged to put forward their views.

5 (g) Student Feedback on Institutional Governance / Faculty Performance

- Student's feedback is taken by Principal by considering various parameters twice in a year regarding academic performance of faculty who teach them.
- Feedback data is analyzed considering various parameters, and accordingly briefed to respective subject teachers, if any by Principal/HODs.
- Students-parents- meets are conducted in every semester and discussed to get the feedback.
- Feedback from the students regarding college, hostel and other facilities are also

- taken and concerned staff/ committee will solve the problem.
- The institute actively seeks and records feedback from parents during their visits, using a dedicated register to track suggestions and concern. This feedback is carefully reviewed and acted upon, with subsequent changes and outcomes meticulously documented. By maintaining a transparent record o this process, the institute ensures accountability, fosters continuous improvement, and enhances its services to meet the evolving needs of students and parents.
- At every department, suggestion box is provided for students and their suggestions which are get analyzed in every month.

Grievance Redressal Committee in the Institution and Appointment of OMBUDSMAN by the University is formed



Shri SiddheshwarDevasthan, Solapur.

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Date: 01/07/2024

OFFICE NOTIFICATION

GRIEVANCE REDRESSAL MECHANISM (SGRC) (Revised)

In order to ensure transparency by Technical Institution imparting Technical Education for student grievances in respect of malpractices in the conduct of examination or in process of evaluation, Grievance Redressal Committee need to be formed

Therefore, AICTE has notified Regulations for establishment of mechanism for Grievance redressal committee and OMBUDSMAN for all AICTE approved Technical Institution vide No.37-3/Legal/2012 dated 25.05.2012.

As per these regulations and as per Clause 1 of Section 23 of the AICTE Act, 1982(52 of 1987) AICTE, Grievance Redressal Committee is formed.

GRIEVANCE REDRESSAL COMMITTEE (SGRC)

[2023-24 & 2024-25]

(http://grievance.sswcoe.edu.in)

SrNo.	Name of the faculty	Designation	Position	Email & Mobile. No.
1	Dr. T.A. Chavan	Principal	Chairperson	principal@sswcoe.edu.in 9689934809
2	Dr. A.S. Patil	Associate Professor HOD (F.Y.Btech)	Faculty Member	aspatil@sswcoe.edu.in 9922641001
3	Prof. B.A. Bachute	Assistant Professor (E&TC Engineering)	Faculty Member	babachute@sswcoe.edu.in 8856933397
4	Prof.P. K.Digge	Assistant Professor (Electrical Engineering)	Co-ordinator & Faculty member	pdwalmiki@sswcoe.edu.in 7066075100
5	Ms. Patil Arpita Vijay	Student Member (S.Y. Btech CSE Dept)	Special Invitee	arpitavp10@gmail.com 7499146899

The students, who are willing to launch complaints, if any, shall send their representation for redressal of their grievances to the Students Grievance Redressal Committee.

Note: Prof. Waikar Milind Laxmanrao (Retrired professor and has also worked as Head of the Department at SGGS Institute of Engineering & Technology, Nanded) is appointed as ombudsman by University DBATU, Lonere

Dr. T. A. Chavan PRINCIPAL



Shri Siddheshwar Devasthan, Solapur.

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Date: 01/07/2024

Notification (Revised)

Ref:- Anti Ragging Notification dated 04/09/2023.

In View of the directive of the Honorable Supreme Court in SLP No. 24295 of 2006 dated 16-05-2007 and in Civil Appeal number 887 of 2009,dated 08-05-2009,to prohibit ,prevent and eliminate the courage of ragging, in exercise of the powers conferred under section 23 read with section10(b),(g), of AICTE Act,1987,the All India Council for Technical Education has notified Regulations for prevention and prohibition of ragging in AICTE approved Technical Institution vide No. 37-3/Legal/AICTE/2009 dated 01.09.2009 and Maharashtra Prohibition of Ragging Act,1999.

As per the directives of Honorable Supreme Court in SLP No. 24295 of 2006 dated 16-05-2007 and in Civil Appeal number 887 of 2009, dated 08-05-2009, the composition of "Anti-Ragging Committee and Anti-Ragging Squad" is as given below. The period of this committee will be for two years i.e. for A.Y. 2023-24 & 2024-25.

ANTI RAGGING COMMITTEE (Revised 2023 - 2024 & 2024 - 2025)

Sr. No.	Name of Staff	Designation	Designation in Committee	Contact No.
1	Dr. T. A. Chavan	Principal	Chairman	9689934809
2	Dr. A. S. Patil	Associate Professor	Member	9922641001
3	Prof. S. K. Bhargande	Assistant Professor	Member	7767844111
4	Prof. P. J. Kothawale	Assistant Professor	Member & Coordinator	8308089311
5	Prof. V. G. Kakade	Assistant Professor	Member	9028594657
6	Prof. S. P. Patil	Assistant Professor	Member	8856933397
7	Prof. V. C. Jetagi	Assistant Professor	Member	8484827290
8	Smt. Sangita Lagshetti	Hostel Warden	Member	8390085678
9	Ms. Suhani Nalwar	Student	Student Representative	9890029821
10	Ms. Amruta Kale	Student	Student Representative	9421416458
11	Mrs. Ambika Patil	Parent	Parents Representative	9657391046

ANTI RAGGING SQUAD (Revised 2023 - 2024 & 2024 - 2025)

Sr. No.	Name of Staff	Designation in Committee
1	Prof. P. J. Kothawale	Member & Coordinator Anti Ragging Squad
2	Prof. S. K. Bhragande	Member, Anti Ragging Squad
3	Prof. S. P. Patil	Member, Anti Ragging Squad
4	Prof. V. C. Jetagi	Member, Anti Ragging Squad
5	Adv. V. C. Achaknalli	Advocate, Anti Ragging Squad



(Dr. T. A. Chavan PRINCIPAL https://sswcoe.edu.in/grivence.html

Internal Complaint Committee (ICC) 5 (l)



Snri SiddnesnwarDevastnan, Solapur.

Shree Siddheshwar Women's College of Engineering, Solapur.

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Date: 14/9/2024

NOTIFICATION

As per the AICTE requirements and Ministry of Human Resource Development, Government of India, our college should have Internal Complaints Committee (ICC). The period of this committee will be for three academic years i.e. from AY 2024-25 to 2026-27. The composition of ICC is as follows

Internal Complaints Committee (ICC) / "Sakhi - Savitri" Committee [Grievance (Womens) Redressal Forum]

Γ	Sr.	Name of Staff	Designation	Designation in	Contact
	No.	Name of Staff	in Institute	committee	Details
	1	Prof. S. S. Kulkarni	Assistant Professor EE	Presiding Officer (senior level women faculty)	sskulkarni@sswcoe.edu.in
ľ	2	Dr. B. P. Sathe	Assistant Professor GSE	Member teaching faculty	bpsathe@sswcoe.edu.in
	3	Prof T.S. Deshmukh	Assistant Professor CSE (AI&DS)	Member teaching faculty	tsdeshmukh@sswcoe.edu.in
Γ	4	Mrs. M. M. Kulkarni	Librarian	Member nonteaching faculty	mmkulkarni@sswcoe.edu.in
ľ	5	Mr. D. R. Berungikar	Office Superintendent	Member nonteaching faculty	drberungikar@sswcoe.edu.in
	6	Ms. Nidaharmain M. Maigeri	Student Representative TY-CSE (A)	Student Member	nidamaigeri@gmail.com
	7	Ms. Swapnali D. Khatake	Student Representative TY- E&TCE	Student Member	entc23- sdkhatake@sswcoe.edu.in
	8	Ms Piyusha P Shenmare	Student Representative TY-CSE (AIDS)	Student Member	ppshenmare2004@gmail.com
	9	Mrs Ankita Sundarlal More	Police Amaldar As member of Sakhi- Savitri	Police Member	7499588199
	10	Dr. Padmaja Gandhi	Student Counselor and Doctor	Member	9561411322 / 9881018967
	11	Prof U.S. Bhandari	Assistant Professor E&TCE	Member And co ordinator	usbhandari@sswcoe.edu.in



(Dr. T.A.Chayan) PRINCIPAL



Shri Siddheshwar Devasthan, Solapur.

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Ref: SSWCOE/OFF/2024/9702

Date: 30/08/2024

Office Order

As per the Schedule Class & Schedule Tribe (Prevention of Atrocities) ACT 1989 No. 33 of 1989 dated 11.09.1989 & as per the guidelines of AICTE, we need to establish committee for SC/ST at our institute. Therefore the composition of SC/ST Committee/Cell is as given below:

SC/ST Committee for Academic Year 2024-25

Sr.No	Name of the Member	Position held in SC/ST Cell	Position held in the institute
1.	Dr. T. A. Chavan	Chairman	Principal
2.	Prof. S. D. Shelar	Co-ordinator	Assistant Professor
3.	Prof. D. B. Parase	Member(Teaching)	Assistant Professor
4.	Prof. S. A. Malvekar	Member(Teaching)	Assistant Professor
5.	Prof. B. S. Murkute	Member(Teaching)	Assistant Professor
6.	Mr. D. R. Berungikar	Member(Non-Teaching)	Office Superintendent

The Committee/Cell may conduct regular remedial classes on life skills, personality development, writing assignment, making presentation, as well as Hindi & local language classes. This cell is also expected to organize interactive sessions and informal meetings with the students to attain their personal, Social and academic problems.

Hence the notification.

Dr. T. A. Chavan (Principal)



Shri Siddheshwar Devasthan, Solapur.

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Date: 23-06-2024

OFFICE NOTIFICATION

As per UGC act 2016 (12th Plan guidelines) for Establishment and Monitoring of the IQACs in College and "Revised Guidelines of IQAC" (Internal Quality Assurance Cell) and submission of AQAR (Annual Quality Assurance Report), 2013, following committee is formed.

The Composition of Internal Quality Assurance Cell (IQAC) (2023-24 to 2024-25)

S.N.	Name	Position held in IQAC	Position held in the Institution
1	Dr. T.A.Chavan	Chairperson	Principal
2	Dr. R.R.Yelikar	Member	Trustee & BoG Member of SSWCOE
3	Shri. Sharad Krishna Thakre	Member (Employer)	BoG Member of SSWCOE & MD, Laxmi Hydraulics Pvt .Ltd.,Solapur,
4	Shri.Vijaykumar Barbade	Member (Nominee from Local Society)	Ex. Deputy Project Director, ATMA
5	Dr.S.P.Rajguru	Member	Professor, Arts & Commerce College, Madha
6	Prof. G.R.Dharane	Member (Senior Teacher)	Principal, SSWP, Solapur
7	Dr. A.S.Patil	Member (Senior Teacher)	HOD F.Y.B.Tech
8	Prof.S.M.Gungewale	Member (Senior Teacher)	HOD CSE
9	Prof. V.V.Shirashyad	Member (Senior Teacher)	HOD IT
10	Prof. S.C.Mhamane	Member (Senior Teacher)	HOD E&TC
11	Prof.A.A.Phatak	Member (Senior Teacher)	HOD CSE (AI&DS)
12	Prof. D.S. Waghmode	Member(Senior Teacher)	HOD Electrical Engg.
13	Dr.S.B.Thigale	Member (Senior Teacher)	NAAC Coordinator
14	Mr.D.R.Berungikar	Member	Office Superintendent (O.S.)
15	Ms. Kshitija Chakote	Member	Student B.Tech CSE
16	Prof. S.J.Madki	Coordinator & Member Secretary	Assistant Professor, F.Y.B.Tech

Solapur F

(Dr. T. A. Chavan) Principal

6(A)	Programmes UG Approved by AICTE				
Sr. No.	Programme	Sanction Intake	Accreditation		
1.	Computer Science & Engineering	120			
2.	Computer Science & Engineering(Artificial Intelligence and Data Science)	90	Not Eligible as our Institute is newly Started from A. Y. 2019- 20		
3.	Electronics & Telecommunication Engineering	60			
4.	Electrical Engineering	30			
5.	Electronics & Computer Engineering	60			
	Total	360			

Cut off Marks of admission

First Year Category wise - Cut-off Marks

Sr. No.	Branch	Category	2020-21	2021-22	2022-23	2023-24
- 1,01		Open	87.49%	88.38%	46.44%	61.29
		SC /ST	75.63%	79.94%	2.75%	15.91
		DT/VJ(A)	61.56%	79.14%	31.77%	41.16
1		NT1(B)	81.06%		39.60%	56.65
		NT2(C)	83.63%	81.77%	29.33%	54.62
	Computer Science& Engineering	NT3 (D)	83.75%		-	16.23
		OBC	81%	87.14%	16.09%	52.55
		SBC		87.43%	16.09%	
		EWS		90.06%	45.98%	59.68
		TFWS			67.44%	76.04
		Pwd+Defence			-	
		ACAP			1.78%	28.60
		Open			17.83%	47.38
		SC			10.64%	32.88
		DT/VJ(A)			-	
2		NT1(B)			-	28.62
		NT2(C)			14.99%	42.55
	Artificial Intelligence and Data Science	NT3 (D)			17.83%	
		OBC			8.38%	37.12
		SBC			-	
		EWS			29.93%	49.55
		TFWS			-	66.95
		ACAP				13.84
		Open	61.94%	73.31%	5.55%	29.99
	Electronics &	SC		64.06%	-	13.13
	Telecommunication	DT/VJ(A)			-	7.92
		NT1(B)			-	24.54

3		NT2(C)			-	
		NT3 (D)			-	
		OBC		68%	-	15.93
		SBC			-	
		EWS		70.53%	3.35%	55.27
		TFWS			-	
		Pwd+def			-	
		ACAP			7.78%	
		Open	64.97%	72.29%	-	11.11
		SC			-	-
		DT/VJ(A)			-	-
		NT1 (B)			-	-
4	Electrical	NT2 (C)			-	-
	Engineering	NT3 (D)			-	-
		OBC			-	-
		SBC			-	-
		EWS		68.76%	17.53%	6.10
		TFWS			-	-
		Open				13.60
		SC				-
		DT/VJ(A)				-
		NT1 (B)				-
	Electronic 0 Comments	NT2(C)				-
5	Electronics & Computer Engg	NT3 (D)				-
		OBC				0.40
		SBC				-
		EWS				28.60
		TFWS				25.30
		ACAP				47.38

Direct Second Year Category wise - Cut-off Marks

Sr.	Branch	Category	2020-	2021-	2022-	2023-
ЭГ.	Dialicii	Category	2020- 21			2023-
No.			21	22	23	2024
	Computer Science& Engineering	Open	87.49%	88.38%	82.06%	82.93%
	0 1 0	SC /ST	75.63%	79.94%	60.46%	82.57%
		DT/VJ (A)	61.56%	79.14%	68.69%	_
		NT1(B)	81.06%		72.86%	84.29 %
_		NT2 (C)	83.63%	81.77%	75.94%	84.69%
1		NT3 (D)	83.75%		75.00%	_
		OBC	81%	87.14%	79.37%	82.57%
		SBC		87.43%	79.31%	82.51%
		EWS		90.06%	84.86%	82.17%
		TFWS				-
		Pwd+Defence			83.26%	82.57%
		ACAP			76.34%	80.51%
	Artificial Intelligence and Data Science	Open			75.31%	81.81%
		SC			67.27%	75%
		DT/VJ (A)				77.77%
		NT1(B)			73.83%	-
		NT2 (C)			73.26%	75.54%
2		NT3 (D)				-
		OBC			68.63%	-
		SBC			64.55%	81.19%
		EWS			77.43%	81.43%
		TFWS				-
		Pwd+Defence				81.81%
		ACAP			67.56%	-

Electronics &	Open	61.94%	73.31%	61.12%	72.40%
Telecommunication	SC		64.06%	58.63%	65.66%

7			Faculty Details		
Sr. No.	Sr. No.	Faculty Name	Branch	Designation	Permanent/ Adjunct
1	1	Dr. T. A. Chavan	Computer Science & Engg	Principal & Professor	Regular
		Computer S	Science And Engineering	g Department	
2	2	Prof. Savita Mohan Gungewale	Computer Science & Engg	Assistant Professor	Regular
3	3	Prof. Jyoti Ishwar Nandalwar	Computer Science & Engg	Assistant Professor	Regular
4	4	Prof. Dipali Bajrang Parase	Computer Science & Engg	Assistant Professor	Regular
5	5	Prof. Geetanjali Ramchandra Kulkarni	Computer Science & Engg	Assistant Professor	Regular
6	6	Prof. Rohit Maruti Pawar	Computer Science & Engg	Assistant Professor	Regular
7	7	Prof. Pallavi Suchit Pandhare	Computer Science & Engg	Assistant Professor	Regular
8	8	Prof. Prachi Shivaji Sable(Teli)	Computer Science & Engg	Assistant Professor	Regular
9	9	Prof. Yoginath Revansidhapa Kalshetty	Computer Science & Engg	Assistant Professor	Ad-hoc
10	10	Prof. Annapurna Kalpesh Salunke	Computer Science & Engg	Assistant Professor	Ad-hoc
11	11	Prof. Vijendra Chandrakant Lad	Computer Science & Engg	Assistant Professor	Ad-hoc
12	12	Prof. Prathamesh Siddharam Birajdar	Computer Science & Engg	Assistant Professor	Ad-hoc
13	13	Prof. Vinashri Gaurang Kakade	Computer Science & Engg	Assistant Professor	Ad-hoc
14	14	Prof. Pooja Amol Katare	Computer Science & Engg	Assistant Professor	Ad-hoc
15	15	Prof. Manjitsing Nageshsing Chandele	Computer Science & Engg	Assistant Professor	Ad-hoc

	Con	nputer Science and Engir	neering (Artificial Intell	igence & Data S	Science			
16	1	Prof. Amol Anand Phatak	Computer Science & Engg(AI & DS)	Assistant Professor	Regular			
17	2	Dr. Somnath B. Thigale	Computer Science & Engg(AI & DS)	Associate Professor	Ad-hoc			
18	3	Prof. Vinay Vishwanath Shirshyad	Computer Science & Engg(AI & DS)	Assistant Professor	Regular			
19	4	Prof. Preeti Jayraj Kothawale	Information Technology	Assistant Professor	Regular			
20	5	Prof. Elia Ratnaraj Chandane	Computer Science & Engg(AI & DS)	Assistant Professor	Regular			
21	6	Prof. Sushant Dadasaheb Shelar	Computer Science & Engg(AI & DS)	Assistant Professor	Ad-hoc			
22	7	Prof. Trupti Sanjay Deshmukh	Computer Science & Engg(AI & DS)	Assistant Professor	Regular			
23	8	Prof. Prajakta Vasant Kale	Computer Science & Engg(AI & DS)	Assistant Professor	Ad-hoc			
24	9	Prof. Anupreeta Bidarkote	Computer Science & Engg(AI & DS)	Assistant Professor	Ad-hoc			
	Electronics & Computer Engineering							
25	1	Dr. Vijaysinh Gopal Chavan	Electronics & Computer Engg	Associate Professor	Ad-hoc			
26	2	Prof. Imran Jaleel Shaikh	Electronics & Computer Engg	Assistant Professor	Ad-hoc			
27	3	Prof. Kanchan Deshmane	Electronics & Computer Engg	Assistant Professor	Ad-hoc			

		Electronic	s & Telecommunication	Engineering	
28	1	Prof. Sanjeev Chandrashekhar Mhamane	Electronics & Telecommunication Engg	Assistant Professor	Regular
29	2	Prof. Madhavi Ranjeet	Electronics &	Assistant	Regular
-9	_	Patil	Telecommunication Engg	Professor	rioguiui
30	3	Prof. Pooja Kalyanrao	Electronics &	Assistant	Regular
		Biradar	Telecommunication Engg	Professor	
31	4	Prof. Bhagyashri Amol Bachute	Electronics & Telecommunication	Assistant Professor	Regular
			Engg		D I
32	5	Prof. Amruta Onkar Nimbargikar	Electronics & Telecommunication Engg	Assistant Professor	Regular
33	6	Prof. Amruta Krishna	Electronics &	Assistant	Regular
		Gadgoli	Telecommunication Engg	Professor	
34	7	Prof. Sonali Sameer Patil	Electronics & Telecommunication Engg	Assistant Professor	Ad-hoc
35	8	Prof. Digge Payal Kiran	Electronics & Telecommunication Engg	Assistant Professor	Regular
36	9	Prof. Varasha Chandrashekhar Jetagi	Electronics & Telecommunication Engg	Assistant Professor	Ad-hoc
			Electrical Engineering		
37	1	Prof. Dnyaneshwar Shivaji Waghmode	Electrical Engg	Assistant Professor	Regular
38	2	Prof. Supriya Subhash Kulkarni	Electrical Engg	Assistant Professor	Ad-hoc
39	3	Prof. Sachin Amrut	Electrical Engg	Assistant	Regular
		Malvekar		Professor	
40	4	Prof. Jayashri Arvind Patil	Electrical Engg	Assistant Professor	Regular

41	5	Prof. Shivali Prasanna Patil	Electrical Engg	Assistant Professor	Regular
42	6	Prof. Shital Baswaraj Patil	Electrical Engg	Assistant Professor	Ad-hoc
43	7	Prof. Pooja Harshal Nare(Sapatnekar)	Electrical Engg	Assistant Professor	Ad-hoc
44	8	Prof. Urmila Santosh Bhandari	Electronics & Telecommunication Engg	Assistant Professor	Ad-hoc
45	9	Prof.Jayashree Dilip Kolhe	Electronics & Telecommunication Engg	Assistant Professor	Ad-hoc
		General	Science & Engineering D	epartment	
46	1	Dr Avinash Shrishail Patil	General Science & Engg	Associate Professor (Chemistry)	Regular
47	2	Dr. Bhaurao Prakash Sathe	General Science & Engg	Assistant Professor (Chemistry)	Ad-hoc
48	3	Dr. M. A. Kazi	General Science & Engg	Assistant Professor (Maths)	Ad-hoc
49	4	Prof. Shirshail Sidramappa Dulange	General Science & Engg	Assistant Professor (Civil)	Ad-hoc
50	5	Dr. Santosh Jaykumar Madki	General Science & Engg	Assistant Professor (Mech)	Regular
51	6	Prof. Suvarna Kisanrao Bhargande	General Science & Engg	Assistant Professor (Physics)	Ad-hoc
52	7	Prof. Ashwini Siddharam Nimbane	General Science & Engg	Assistant Professor (English)	Ad-hoc
53	8	Dr. Reshma NarayanDhanavade	General Science & Engg	Assistant Professor (Physics)	Ad-hoc
54	9	Prof. Sandhya vijay Palmur	General Science & Engg	Assistant Professor (Maths)	Ad-hoc
55	10	Prof. Hajra Begum Yusuf Shaikh	General Science & Engg	Assistant Professor	Ad-hoc

				(Maths)	
56	11	Prof. Ambulge Priyanka Mahajan	General Science & Engg	Assistant Professor	Ad-hoc
				(Maths)	
57	12	Prof. Akshay Prakash	General Science & Engg	Assistant	Ad-hoc
		Gajare		Professor	
				(Mech)	
58	13	Duraf Chinala Connei Chinaii	General Science & Engg	Assistant	Ad-hoc
		Prof. Shinde Suraj Shivaji		Professor (Civil)	
59	14	Prof. Balaji Murkute	General Science & Engg	Assistant	Ad-hoc
39	14		General Science & Lings	Professor	Tid floc
				(Maths)	
60	15	Prof. Payal Chakote	General Science & Engg	Assistant	Ad-hoc
				Professor	

8	Faculty Profile								
	Profile of Principal								
i.	Name	Dr. T. A. Chavan							
ii.	Date of Birth	01-06-1973							
iii.	Unique ID	1-7400135317							
iv.	Education Qualification	B.E.(Computer Engineering)	M.Tech. (Computer Engineering)	Ph.D (Computer Science and Engineering)					
v.	Work Experience	25Years							
	Teaching	19 Years							
	• Research	05 Years							
	• Industry	01 Years							
	• Others								
vi.	Area of Specialization	Mobile Communication	n						
vii.	Course taught at Diploma/UG/PG	UG: Data Structure, Operating System, DBMS, Distributed System, Mobile Computing, DAA	PG: Mobile Comp Algorithm, Distrib hoc Network						
viii.	Research Guidance	UG: 34 Groups (136 Students)	PG: 18 Students						
ix.	Projects Carried out	34	18						
X.	Patents			02					
xi.	Technology Transfer		NPTEL- DBMS, DAA	-					
xii.	Research Publication	National-02	International-25						
xiii.	No. of Books Published with details	Data Structure							

General Science and Engineering Department Faculty Profile

i.	Name	Dr. A. S. Patil		
ii.	Date of Birth	28-12-1974		100
iii.	Unique ID	1-7385931000		
iv.	Education Qualification	B. Ed.	M. Sc.(Chemistry)	Ph.D
v.	Work Experience			
	• Teaching	28 Years		
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Organic Chemist	try, Polymer Chemi	stry
vii.	Course taught at Diploma/UG/PG	UG- Engineering	g Chemistry	
viii.	Research Guidance			
ix.	Projects Carried out			
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-05	International-14	
xiii.	No. of Books Published	Text book of	Text book of	
	With	Engineering	Engineering	
	Details	Chemistry-Tech		
		Max	Shradda	
		Publication,	Publication,	
		Pune.	Solapur	

i.	Name	Madki Santosh Jaykuma		
ii.	Date of Birth	25-12-1976		7 5
iii.	Unique ID			
iv.	Education Qualification	B.E.(Mechanical Engineering)	M.Tech. (Production Engineering)	Ph.D
v.	Work Experience	23 Years		
	• Teaching	23 Years		
	• Research	05 Years		
	• Industry	-		
	• Others	-		
vi.	Area of Specialization	Metal Matrix Composites	3	
vii.	Course taught at Diploma/UG/PG	UG: Engg Graphics, Basic Mechanical Engineering, Nonconventional Energy Sources, Project Management,Product Design, Tool Engineering, Automobile Engg.	Methodology,F Management	Project
viii.	Research Guidance	UG: 38 Groups (152 Students)	PG: 14 Student	ts
ix.	Projects Carried out	38	14	
x.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-03	International-	20
xiii.	No. of Books Published with details	 Basic Mechanical Engineering 2007 Basic Mechanical Engineering 2012 Professional Ethics 		
		& Human Values 2015		

i.	Name	Dr. B. P. Sathe		
ii.	Date of Birth	31-05-1986		(A) (B)
iii.	Unique ID	1-44238146978		
iv.	Education Qualification	B. Sc. (Chemistry, Physics, Maths.)	M. Sc. (Chemistry)	Ph. D. (Chemistry)
v.	Work Experience	(10 Years)		
	Teaching	03 Years	07 Years	
	• Research			05 Years
	• Industry			
	• Others			
vi.	Area of Specialization	Organic Chemistry, Nar	no-particals	
vii.	Course taught at Diploma/UG/PG	UG: Organic reaction and rearrangement, Inorganic chemistry.	PG: Introduction t Chemistry, Drug a Development, Spe	ction and
viii.	Research Guidance	UG: 20 Students	PG: 30 Students	
ix.	Projects Carried out	10	15	
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-06	
xiii.	No. of Books Published with details			

i.	Name	Prof. S. K. Bhargande		
ii.	Date of Birth	01-05-1981		
iii.	Unique ID	1-7389182093		
iv.	Education Qualification	B. Ed.	M. Sc. (Physics)	Ph. D(Pursuing)
v.	Work Experience	17 Years		
	 Teaching 	17 Years		
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Solid State Physi	cs	
vii.	Course taught at Diploma/UG/PG	Physics		
viii.	Research Guidance			
ix.	Projects Carried out			
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-04	
xiii.	No. of Books Published with details	A Text book of E Solapur	ngg. Physics Shraddh	a Publication,

i.	Name	Prof. A. S. Nin	nbane	
ii.	Date of Birth	20-03-1987		
iii.	Unique ID	1-7391322222		
iv.	Education Qualification		M. A. (English)	M. B. A.(HR & Marketing)
v.	Work Experience	11 Years		
	 Teaching 	11 Years		
	Research			
	• Industry			
	• Others			
vi.	Area of Specialization	English Litera	ture	
vii.	Course taught at Diploma/UG/PG	English, Com Development	nunication Skills, I of Life Skills	Management,
viii.	Research Guidance			
ix.	Projects Carried out			
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-o	International-o	
xiii.	No. of Books Published with details			

i.	Name	Prof. A. I. Shaikh		
ii.	Date of Birth	11-02-1998		88
iii.	Unique ID			
iv.	Education Qualification	B.Sc. (Mathematics)	M.Sc. (Mathematics)	B.Ed.
v.	Work Experience	0.3 Years		
	• Teaching	0.3 Years		
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization			
vii.	Course taught at Diploma/UG/PG			
viii.	Research Guidance			
ix.	Projects Carried out			
x.	Patents			
xi.	Technology Transfer			
xii.	Research Publication			
xiii.	No. of Books Published with details			

i.	Name	S.V.Palmur		A
ii.	Date of Birth	27-12-1984		190
iii.	Unique ID			
iv.	Education Qualification	B.Sc.(Mathematics)	M.Sc.(Mathemati cs) SET (Maths),B.Ed	Ph.D* (Mathematics)
v.	Work Experience	13 Years		
	 Teaching 	13 Years		
	• Research	01 Years		
	• Industry	Nil		
	• Others	Nil		
vi.	Area of Specialization	Fluid Mechanics (Boun	dary Layer Equation	ns)
vii.	Course taught at Diploma/UG/PG	UG: Engineering Maths I, II,III Numerical Methods	PG: Nil	
viii.	Research Guidance	Nil	Nil	
ix.	Projects Carried out	Nil	Nil	
x.	Patents	Nil	Nil	
xi.	Technology Transfer	Nil	Nil	
xii.	Research Publication	National-Nil	International-1	
xiii.	No. of Books Published with details	Nil		

i.	Name	Prof. H .Y. Shaikh		
ii.	Date of Birth	14-05-1997		60
iii.	Unique ID			
iv.	Education Qualification	B.Sc.(Mathematics)	M.Sc (Mathematics)	B.Ed
v.	Work Experience	2.8 years,		
	• Teaching	2 Years	8 Months	
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Mathematics		
vii.	Course taught at Diploma/UG/PG	Diploma :Engineering mathematics	PG: Elementary M	athematics
viii.	Research Guidance			
ix.	Projects Carried out			
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication			
xiii.	No. of Books Published with details			

i.	Name	Ms. P M Ambulge		
ii.	Date of Birth	02-06-1989		(a.e.
iii.	Unique ID			
iv.	Education Qualification	M.Sc., (Mathematics)	M.Phil.(Mathem atics	B.Ed.(Sci & Maths Method)
v.	Work Experience	Years		
	Teaching	5 Years		
	Research	o Years		
	• Industry	0 Years		
	• Others			
vi.	Area of Specialization	Differential Equations	,	
vii.	Course taught at Diploma/UG/PG	Diploma M1, M2,	UG: M1, M2, proba Random Process	bility and
viii.	Research Guidance			
ix.	Projects Carried out			
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-0	
xiii.	No. of Books Published with details			

i.	Name	Dr. R. N. Dhanawade		
ii.	Date of Birth	09-01-1991		
iii.	Unique ID	-		
iv.	Education Qualification	M.Sc.(Physics)	B.Ed.	Ph.D. (Physics)
v.	Work Experience	4.5 Years		
	Teaching	4.5 Years		
	• Research	00 Years		
	• Industry	00 Years		
	• Others			
vi.	Area of Specialization	Gas Sensor		
vii.	Course taught at Diploma/UG/PG	-	-	
viii.	Research Guidance	-	-	
ix.	Projects Carried out	-	-	
х.	Patents	-	-	
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-3	
xiii.	No. of Books Published with details	-	-	-

i.	Name	Prof. S.S.Shinde		
ii.	Date of Birth	17-07-1993		(3.6)
iii.	Unique ID			
iv.	Education Qualification	B. E. (Civil Engineering)	M.Tech. (Structural Engineering)	
v.	Work Experience	5 Years		
	Teaching	1 Years		
	• Research			
	• Industry	4 Years		
	• Others			
vi.	Area of Specialization	Structural Engineering	1	
vii.	Course taught at Diploma/UG/PG	UG: Engineering Mechanics		
viii.	Research Guidance			
ix.	Projects Carried out			
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-	0
xiii.	No. of Books Published with details			

i.	Name	Prof. S.S. Dulange		-
ii.	Date of Birth	14-07-1982		3
iii.	Unique ID			
iv.	Education Qualification	(DCE) Diploma in Civil Engineering	B.E. (Civil Engineering)	M.E. (Construction Management)
v.	Work Experience	15 Years		
	• Teaching	12 Years		
	• Research	00 Years		
	• Industry	03 Years		
	• Others			
vi.	Area of Specialization	Construction Managemen	t	
vii.	Course taught at Diploma/UG/PG	Diploma: Estimating & Costing, Concrete Technology, Engineering Mechanics,	UG: Professional Practices, Foundation Engineering, Town & Urban Planning	
viii.	Research Guidance	UG:	PG:	
ix.	Projects Carried out			
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-00	International-02	2
xiii.	No. of Books Published with details			

i.	Name	Prof. B. S. Murkute		
ii.	Date of Birth	26-07-1987		
iii.	Unique ID	7650 9290 3749		
iv.	Education Qualification	B.Sc. (Mathematics, Statistics, Computer Science)	M.Sc. (Mathematics)	
v.	Work Experience	7.7 Years		
	Teaching	7.7 Years		
	Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Applied Mathematics		
vii.	Course taught at Diploma/UG/PG	UG: Engg. M-I, M-II, M-III, B.Sc. Group Theory, Linear Algebra, Ring Theory	PG: M.E. (ENTC)[\text{Y} Embeded System] Distributions & Lir	Probability

Computer Science and Engineering Department Faculty Profile

i.	Name	Prof. S. M. Gungev	vale	
ii.	Date of Birth	21-09-1990		
iii.	Unique ID	1-7389182384		
iv.	Education Qualification	B.E. (Computer Science & Engg.)	M.E. (Computer	Science & Engg.)
**	Work Experience	00:		
V.	Teaching	9.5 Years 6.11 Years		
	Research	0.11 1ears		
		o.8 Years		
	IndustryOthers	0.6 fears		
	• Others	D		
vi.	Area of Specialization	Programming Languages		
vii.	Course taught at Diploma/UG/PG	Diploma: Java, Advanced Java, Computer Graphics, VB.Net	Engg: Java, Adva Technology	nced Java, Web
viii.	Research Guidance			
ix.	Projects Carried out	02		
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-o	International-01	
xiii.	No. of Books Published with details			

i.	Name	Prof. J. I. Nandalwar		-
ii.	Date of Birth	27-06-1981		90
iii.	Unique ID	1-9325155448		577
iv.	Education Qualification	B.E. (Computer Science & Engg.)	M-Tech. (Computer Science & Engg.)	PhD Persuing
v.	Work Experience	16.6 Years		
	Teaching	16.6 Years		
	Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Operating system, Com	puter Networks	
vii.	Course taught at Diploma/UG/PG	Operating system Computer Network Data Structure Computer Orga.	Computer Network Organization	
viii.	Research Guidance			
ix.	Projects Carried out			
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National – 04	International -o	2
xiii ·	No. of Books Published with details			

i.	Name	Prof Parase Dipali Bajra	ing	
ii.	Date of Birth	10/01/1989		
iii.	Unique ID			
iv.	Education Qualification	ME(CSE)	BE(CSE)	
v.	Work Experience	Total -09 Yrs		
	Teaching	07		
	• Research	-		
	• Industry	-		
	• Others	-		
vi.	Area of Specialization	Computer Science		
vii.	Course taught at Diploma/UG/PG	UG: Operating System, Compiler Construction, Digital Techniques, Microprocessor		
viii.	Research Guidance	UG: 05	Diplom	a: 07
ix.	Projects Carried out	06	07	
X.	Patents	-		
xi.	Technology Transfer			
xii.	Research Publication	International-06		
xiii.	No. of Books Published with details	-		

i.	Name	Prof. P. S. Pandhare.		
ii.	Date of Birth	26-02-1987		
iii.	Unique ID			
iv.	Education Qualification	B.E.(Computer Engineering)	M.E (Computer Engineering)	
v.	Work Experience	6.5 years		
	Teaching	6.5 years		
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization			
vii.	Course taught at Diploma/UG/PG	UG: Data structure, Web technology, Computer Network, Computer graphics, Blockchain technology, ethics and cyber law, System programming, compiler construction		
viii.	Research Guidance			
ix.	Projects Carried out	10		
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-02		
xiii.	No. of Books Published with details			

i.	Name	Prof. R. M. Pawar		
ii.	Date of Birth	20 -01-1988		
iii.	Unique ID			
iv.	Education Qualification	B.E.(Computer Engineering)	M.E. (Computer Engineering)	
v.	Work Experience	5 Years 10 Months		
	• Teaching	4 Years 10 Months		
	• Research			
	• Industry	01 Years		
	• Others			
vi.	Area of Specialization	Data Mining		
vii.	Course taught at Diploma/UG/PG	UG: Computer Fundamental, C++, Data Structure, Java, Distributed System.		
viii.	Research Guidance	UG: 06 Groups (24 Students)		
ix.	Projects Carried out	06		
x.	Patents			
xi.	Technology Transfer			
xii.	Research Publication		International-	l
xiii.	No. of Books Published with details	03 Books published in gaming domain		

i.	Name	Prof.G.R.Kulkarni		
ii.	Date of Birth	13-08-1986		60
iii.	Unique ID			
iv.	Education Qualification	B.E.(Computer Engineering)	M.Tech. (Computer Engineering)	
v.	Work Experience	15 Years		
	 Teaching 	15 Years		
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Computer Science & En	gineering	
vii.	Course taught at Diploma/UG/PG	UG: Data Structure, Operating System, DBMS, Distributed System, DAA,Cloud Computing,Artifical Intelligence,Microproc essor,Programming in JAVA		
viii.	Research Guidance	UG: 11 Groups (44 Students)	PG:	
ix.	Projects Carried out	34	18	
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication			
xiii.	No. of Books Published with details			

i.	Name	Prof. Y. R. Kalshetty		
ii.	Date of Birth	06-11-1981		
iii.	Unique ID	1-10553811721		
iv.	Education Qualification	B.E.(Computer Science and Engineering)	M.E. (Computer Engineering)	
v.	Work Experience	20 Years		
	• Teaching	15 Years		
	• Research	01 Years		
	• Industry	02 Years		
	• Others	02 years – Startup II	ndia	
vi.	Area of Specialization	Data Structure, Algo	rithm & Optimiza	tion
vii.	Course taught at Diploma/UG/PG	Programming, OOP,	UG: Data Structure, DAA, Python, C Programming, OOP, VC++ (SDK), Web Technology, System Programming.	
viii.	Research Guidance	UG: 32 Groups (128 Students)	PG: 13 Students	
ix.	Projects Carried out	32	13	01 (BARC Mumbai)
x.	Patents		-	
xi.	Technology Transfer			
xii.	Research Publication	National: 02	International:14	
xiii.	No. of Books Published with details			

i.	Name	Mrs. Annapurna K Salur	nke	
ii.	Date of Birth	28-03-1985		(0.0)
iii.	Unique ID			
iv.	Education Qualification	B.E.(Information Technology)	M.E (CSE-SE)	
v.	Work Experience	13+ Years		
	• Teaching	10+ Years		
	• Research	00 Years		
	• Industry	02Years 6months		
	• Others			
vi.	Area of Specialization			
vii.	Course taught at Diploma/UG/PG	UG: Operating System, DBMS, Distributed Systems, ADBMS, Infor mtion and cyber sec., UI/UX,		
viii.	Research Guidance	UG: 8 Groups		
ix.	Projects Carried out	10		
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-04		
xiii.	No. of Books Published with details			

i.	Name	Prof. Vijendra C. Lad		
ii.	Date of Birth	30-05-1990		(0.0)
iii.	Unique ID			
iv.	Education Qualification	B.E.(Computer Science Engineering)	M.E. (Computer Networks)	
v.	Work Experience	7 Years		
	Teaching	0		
	• Research	0		
	• Industry	7 Years		
	• Others			
vi.	Area of Specialization	Computer Networks,Clo (AWS Cloud Practitions	oud Computing er Certified)	
vii.	Course taught at Diploma/UG/PG			
viii.	Research Guidance			
ix.	Projects Carried out			
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-0	International-2	
xiii.	No. of Books Published with details			

i.	Name	Mrs. V.G. Kakade		
ii.	Date of Birth	20-03-1987		
iii.	Unique ID			
iv.	Education Qualification	B.E.(Computer Engineering)	M.Tech. (Computer Engineering)	
v.	Work Experience	10 Years		
	Teaching	10 Years		
	Research	0 Years		
	• Industry	0 Years		
	• Others			
vi.	Area of Specialization	CSE		
vii.	Course taught at Diploma/UG/PG	UG: Artificial Intelligence, Computer Network, computer security Operating System, DBMS, Distributed System,		
viii.	Research Guidance			
ix.	Projects Carried out			
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-00	International-3	
xiii.	No. of Books Published with details	Artificial intelligence		

Computer Science and Engineering(Artificial Intelligence & Data Science) and Information Technology Faculty Profile

i.	Name	Prof. Amol A. Phatak		
ii.	Date of Birth	21/11/1979		100
iii.	Unique ID			
iv.	Education Qualification	B.E. (CSE)	M.E. (CSE)	*Ph.D (CSE)
v.	Work Experience	22 Years		
	• Teaching	22 Years		
	• Research	06 Years		
	• Industry			
	• Others			
vi.	Area of Specialization	Computer Network and Image Processing,	IoT Security, Data	Science,
vii.	Course taught at Diploma/UG/PG	UG: Operating Systems, Computer Graphics, Computer Network, Information and Cyber Security, Database Engineering, DMS, Data Communication	PG: Distributed Sy Information and C Data Mining, IoT	•
viii.	Research Guidance	UG: 42 Groups (208 Students)	PG: 14 Students	
ix.	Projects Carried out	42	14	
х.	Patents	National Indian Patent: 1 Granted, 3 Applied	International Patent: 2 Applied	
xi.	Technology Transfer	Copy right : 1		
xii.	Research Publication	National-03	International-13	
xiii.	No. of Books Published with details			

i.	Name	Prof. V. V. Shirashyad		
ii.	Date of Birth	05-07-1985		
iii.	Unique ID			
iv.	Education Qualification	B.E. (Computer Science & Engineering)	M.Tech. (Computer Science & Engineering)	
v.	Work Experience	12 Years		
	• Teaching	08 Years		
	• Research	00 Years		
	• Industry	04 Years		
	• Others			
vi.	Area of Specialization			
vii.	Course taught at Diploma/UG/PG	UG: Data Structure, Operating System, DBMS, Programming in Java, Mobile Application Development, DAA, Artificial Intelligence, R Programming	PG:	
viii.	Research Guidance	UG: 02 Groups (08 Students)	PG:	
ix.	Projects Carried out			
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-03	International-	
xiii.	No. of Books Published with details	01		

i.	Name	Prof. P. J. Kothawale		
ii.	Date of Birth	01-02-1986		(0.0)
iii.	Unique ID			3
iv.	Education Qualification	B.E.(Information Science & Technology)	M.Tech. (Computer Engineering)	' _
v.	Work Experience	12 Years		
	Teaching	12 Years		
	• Research	00 Years		
	• Industry	00 Years		
	• Others			
vi.	Area of Specialization			
vii.	Course taught at Diploma/UG/PG	Diploma: Data Structure, Operating System, DBMS, Programming with Python,Programming in C, Vb. Net	UG: Data Structure Software Engineer Visualization.	
viii.	Research Guidance	Diploma : 34 Groups (136 Students)	UG: 4 Students	
ix.	Projects Carried out	34	18	
x.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-1	International-1	
xiii.	No. of Books Published with details	Diploma: Manual for Computer Fundamental		

i.	Name	Mr. Elia R Chandane		
ii.	Date of Birth	18-08-1982		
iii.	Unique ID			
iv.	Education Qualification	Dip.(Electronics Engineering)	B.E (EnTC Engineering)	M.E (EnTC Engineering)
V.	Work Experience	14 Years		
	 Teaching 	11 Years		
	• Research	02 Years		
	• Industry	01 Year		
	• Others			
vi.	Area of Specialization	Electromagnetics and C	ommunication	
vii.	Course taught at Diploma/UG/PG	UG: Electromagnetics Engg, Analog Comm, Digital Comm, Broadband Comm, Computer Networks, Radar & Microwave Engg, Computer Arch & Organization	Nor	ne
viii.	Research Guidance	UG: 07 Groups (35 Students)	PG: None	
ix.	Projects Carried out	06	None	
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-02	International-4	
xiii.	No. of Books Published with details	Voltage SAG MITIGATION	Lambert Publications	

i.	Name	Dr. Somnath B. Thigale		
ii.	Date of Birth	03-08-1983		3
iii.	Unique ID	1-7480285759		
iv.	Education Qualification	B.E.(Computer Science & Engineering)	M.E. (Computer Science & Engineering)	Ph.D. (Computer Science and Engineering)
v.	Work Experience	17+ Years		
	• Teaching	11+ Years	Associate Profes	ssor
	• Research	05* Years	CSE-IoT	
	• Industry	06 Years	Sr. Technical Ev	angelist
	• Consultancy	02* Years	Banking & Edu	cation
vi.	Area of Specialization	IoT, AI, ML, OOAD,OOM	D, Software Engi	neering & Testing
vii.	Course taught at Diploma/UG/PG	UG: DS,OOMD, Software Engg., Software Testing	PG: Theory of C ML, Digital Busi	
viii.	Research Guidance	UG: 20+ Groups (100 Students)	PG: 07 Students	
ix.	Projects Carried out	25	07	
x.	Patents	10 Patents Published <u>htt</u>	os://rb.gy/40sdv	<u>d</u>
xi.	Technology Transfer	Banking Applications, College ERP		
xii.	Research Publication	National-10 International-25 https://rb.gy/40sdvd		
xiii.	No. of Books Published with details	10 Books Published <u>http</u>	s://rb.gy/40sdvd	

i.	Name	Prof. S. D. Shelar		
ii.	Date of Birth	21-05-1988		000
iii.	Unique ID			Ö
iv.	Education Qualification	B.E.(Computer Science and Engineering)	M.Tech. (Computer Science and Engineering)	
v.	Work Experience	o6 Years		
	• Teaching	04 Years		
	• Research			
	• Industry	02 Years		
	• Others			
vi.	Area of Specialization	Programming		
vii.	Course taught at Diploma/UG/PG	UG: Java,Advanced Java,Python,Human Computer Interaction, Development Engineering, Operating System, Mobile Application Development using Android		
viii.	Research Guidance			
ix.	Projects Carried out	02		
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication			
xiii.	No. of Books Published with details			

i.	Name	Prof.T.S.Deshmukh		
ii.	Date of Birth	31-07-1990		2.0
iii.	Unique ID			8 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
iv.	Education Qualification	B.E.(Computer Science & Engineering)	M.E. (Computer Science& Engineering)	
v.	Work Experience	4 Years		
	 Teaching 	4 Years		
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization			
vii.	Course taught at Diploma/UG/PG	UG:DBMS,C,C++,Java Programming,Software Eggineering,Testing,Co mputer Network		
viii.	Research Guidance			
ix.	Projects Carried out	2		
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication		International-3	
xiii.	No. of Books Published with details			

i.	Name	Prof. A. A. Bidarkote		
ii.	Date of Birth	15-04-2000		
iii.	Unique ID			
iv.	Education Qualification	BTech(Computer Engineering)	MTech (Computer Science & Engineering)	
v.	Work Experience	5 Months		
	 Teaching 	5 Months		
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Machine Learning		
vii.	Course taught at Diploma/UG/PG	UG: Employability and Skill Development		
viii.	Research Guidance			
ix.	Projects Carried out			
x.	Patents			
xi.	Technology Transfer			
xii.	Research Publication		International-2	
xiii.	No. of Books Published with details			

Electronics and Telecommunication Engineering

https://sswcoe.edu.in/entcf.html

i.	Name	Mr. S. C. Mhamane		
ii.	Date of Birth	16/10/1985		
iii.	Unique ID			
iv.	Education Qualification	B.E.(ECE)	M.E (E&TC)	Ph.D* (ECE)
v.	Work Experience	13		
	• Teaching	11 Years		
	• Research			
	• Industry	02 Years		
	• Others			
vi.	Area of Specialization	Communication		
vii.	Course taught at Diploma/UG/PG	UG: Digital Logic Design, Control System, MPMC,	PG: Mobile Computing,	
viii.	Research Guidance	UG: 15 Groups (60 Students)	PG: 04 Students	
ix.	Projects Carried out			
X.	Patents	02		
xi.	Technology Transfer			
xii.	Research Publication	National-02	International-14	
xiii.	No. of Books Published with details			

i.	Name	Prof. Madhavi Ranjeet l	Patil	
ii.	Date of Birth	30 th March 1986		20
iii.	Unique ID	1-4290336542		
iv.	Education Qualification	B.E.(E&TC),	M.E.(E&TC),	PHD*(ECE)
v.	Work Experience	10 years		
	Teaching	5.10yrs(diploma),	5yrs(Engg.)	
	• Research	-	-	-
	• Industry	-	-	-
	• Others	-	-	-
vi.	Area of Specialization	communication		
vii.	Course taught at Diploma/UG/PG	Diploma: Embedded systems, Elements of Electrical Engineering, communication engineering, Electronic workshop I & II ,Digital electronics, Microprocessor, electronic test and Measurements.		unication te vigital electronics and Iobile nalog
viii.	Research Guidance	Diploma : 5 groups(18students)	UG :5 groups(15 st	udents)
ix.	Projects Carried out	5	5	-
х.	Patents	-	-	-
xi.	Technology Transfer	-	-	-
xii.	Research Publication	International - 3	-	
xiii.	No. of Books Published with details	-	-	-

i.	Name	A O Nimbargikar		
ii.	Date of Birth	02/03/1990		
iii.	Unique ID			
iv.	Education Qualification	B.E(Electronics)	M.E(Electronics)	
v.	Work Experience	3 Years		
	Teaching	1 Years		
	• Research			
	• Industry	2 Years		
	• Others			
vi.	Area of Specialization	Image Processing		
vii.	Course taught at Diploma/UG/PG	Microcontroller and Introduction to analog Communication		
viii.	Research Guidance			
ix.	Projects Carried out			
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication		International-02	
xiii.	No. of Books Published with details			

i.	Name	Prof. P. K. Biradar	
ii.	Date of Birth	28-08-1990	88
iii.	Unique ID	1-9323553385	
iv.	Education Qualification	B.E. (Instrumentation Technology)	M.E.(Electronics)
v.	Work Experience	o2 Year	
	Teaching	02 Year	
	• Research		
	• Industry		
	• Others		
vi.	Area of Specialization	Electronics	
vii	Course taught at Diploma/UG/PG	Digital signal Processing	
i.	Research Guidance		
ix.	Projects Carried out		
x.	Patents		
xi.	Technology Transfer		
xii	Research Publication	International -02	
xii i.	No. of Books Published with details		

i.	Name	Prof. Payal D. Digge		
ii.	Date of Birth	12/02/1997		
iii.	Unique ID			
iv.	Education Qualification	B.E.(Electronics & Telecommunication)	M.Tech (Electronics & Telecommunicati on)	
v.	Work Experience	03		
	Teaching	01		
	• Research	-		
	• Industry	-		
	• Others	02		
vi.	Area of Specialization	Electronics & Telecomn		
vii.	Course taught at Diploma/UG/PG	UG- Embedded system, Microprocessor & Micro-controller		
viii.	Research Guidance	-		
ix.	Projects Carried out	03		
X.	Patents	-		
xi.	Technology Transfer	-		
xii.	Research Publication	International-02		
xiii.	No. of Books Published with details	-		

	Name	Mrs. B. A. B	achute	
ii.	Date of Birth	17/07/90		A (a a)
iii.	Unique ID			
iv.	Education Qualification	B.E.(E&TC)	M.E.[ELECTRO NICS]	
v.	Work Experience	1 Year		
	• Teaching			
	• Research			
	• Industry	1 Year		
	• Others			
vi.	Area of Specialization	ELECTRON	ICS	
vii.	Course taught at Diploma/UG/PG	UG: DIPLOMA IN SOFTWARE TSTING		
viii.	Research Guidance	-		
ix.	Projects Carried out			
X.	Patents	-		
xi.	Technology Transfer	-		
xii.	Research Publication	NATIONAL -		
xiii.	No. of Books Published with details			

i.	Name	Prof. A.K. Gadgoli		
ii.	Date of Birth	24-04-1990		-
iii.	Unique ID			
iv.	Education Qualification	B.E.(Electronics & Tele-Communication)	M.Tech. (Electronics & Tele- Communication)	Ph.D-
v.	Work Experience	07 Years		
	 Teaching 	07 Years		
	• Research	00 Years		
	• Industry	00 Years		
	• Others			
vi.	Area of Specialization	Electronics & Tele-Com	munication	
vii.	Course taught at Diploma/UG/PG	Diploma: Basic & Consumer Electronics, Electronics Measurement, Optical Networks & Satellite Comm.	UG: Analog Communication, Digital Communication	
viii.	Research Guidance	Diploma- 21	UG:4	
ix.	Projects Carried out	07	01	
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication		National-02	
xiii.	No. of Books Published with details			

i.	Name	Prof. V. C. Jetagi		
ii.	Date of Birth	19-11-1996		1
iii.	Unique ID			
iv.	Education Qualification	B.E.(Electronics and Telecommunication Engineering)	M.Tech. (Electronics Engineering)	
v.	Work Experience	0 Years		
	Teaching			
	• Research			
	• Industry			
	• Others			
vi.	Area of Specialization	Electronics Engineering	5	
vii.	Course taught at Diploma/UG/PG			
viii.	Research Guidance			
ix.	Projects Carried out			
X.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-1	
xiii.	No. of Books Published with details			

Electrical Engineering Faculty Profile

i.	Name	Prof. D. S. Waghmode		
ii.	Date of Birth	09-12-1990		
iii.	Unique ID			
iv.	Education Qualification	B.E. (Electrical Engineering)	M (Electrical Powe	
v.	Work Experience	8 Years		
	Teaching	8 Years		
	• Industry	5 Years		
	• Others	-		
vi.	Area of Specialization	Electrical Power System		
vii.	Course taught at Diploma/UG/PG	UG: Power System-I&II, PSOC , HVDC & FACTs	, Network Theory	, EMI, PSA,
viii.	Research Guidance	UG:12 Group		
ix.	Projects Carried out	12		
x.	Patents	04		
xi.	Technology Transfer	-		
xii.	Research Publication	National-01	International-10	
xiii.	No. of Books Published	04		

i.	Name	Prof. S. A. Malvekar		
ii.	Date of Birth	08-10-1994		
iii.	Unique ID			
iv.	Education Qualification	B. E. (Electrical Engineering)	M.Tech. (Power System)	
v.	Work Experience	4 Years		
	 Teaching 	4 Years		
	• Research	2 Years		
	• Industry			
	• Others			
vi.	Area of Specialization	Power system, Power Qualit Renewable Energy	y, Power Elect	ronics &
vii.	Course taught at Diploma/UG/PG	UG: Power System, Electrical Machine & Instruments, HVDC, HVDC & FACTS, Switchgear & Protection, Power Plant Engineering, Basic Electrical Engineering		
viii.	Research Guidance	UG: 1 Group		
ix.	Projects Carried out	1		
x.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-	3
xiii.	No. of Books Published with details	Voltage Sag Mitigation in Dynamic Voltage Restorer (DVR) Based on Ultra Capacitors		

i.	Name	Prof. J. A. Patil		
ii.	Date of Birth	19-11-1997		
iii.	Unique ID			
iv.	Education Qualification	B.E(Electrical and Electronics Engineering)	M.Tech. (Power System Engineering)	
v.	Work Experience	1.3 years		
	Teaching	1.3 Years		
	• Research	0 Years		
	• Industry	0Years		
	• Others			
vi.	Area of Specialization	Power system		
vii.	Course taught at Diploma/UG/PG	UG: Engineering Material Science	PG:	
viii.	Research Guidance	UG:	PG:	
ix.	Projects Carried out			
x.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-0	International-1	
xiii.	No. of Books Published with details			

i.	Name	Prof. S.P.Patil		4
ii.	Date of Birth	19/03/1993		
iii.	Unique ID			
iv.	Education Qualification	B.E.(Electrical ,Electronics & Power)	M.Tech. (E&TC)	
v.	Work Experience	1 years		
	• Teaching	1 Years		
	• Research	0 Years		
	• Industry	0Years		
	• Others			
vi.	Area of Specialization	Power system		
vii.	Course taught at Diploma/UG/PG	UG: High Voltage Engineering , Electrical &Electronics Measurement	PG:	
viii.	Research Guidance	UG:	PG:	
ix.	Projects Carried out			
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-0	International-2	
xiii.	No. of Books Published with details			

i.	Name	Prof S. S. Kulkarni		
ii.	Date of Birth	09-03-1988		
iii.	Unique ID	_		
iv.	Education Qualification	B.E.(Electrical Electronics and Power)	M.Tech. (Power Electronics and Drives)	-
v.	Work Experience	13 Years	,	
	• Teaching	13 Years		
	• Research	-		
	• Industry	-		
	• Others	-		
vi.	Area of Specialization	Reactive power Control	, Power Electronics	and drives
vii.	Course taught at Diploma/UG/PG	Diploma: Basic Electrical Engineering, Electrical Tethnology	Electronics, FACTS and control, Electronic Devices Flexible AC transmitransmission, HV systems, Electrical	er Electronics agineering, Power FACTS, Power Electrical Drives ical Technology, and Circuits, ission and HVDC DC Transmission Technology, angg., Electrical costing, Power mission And Switchgear and
viii.	Research Guidance	UG: 2 Groups (6 Students)	PG: 20 groups (Near Students	r about 65
ix.	Projects Carried out	2	20	
x.	Patents	-	-	1
xi.	Technology Transfer	_	-	-
xii.	Research Publication	National-01	International-07	

Electronics and Computer Engineering Faculty Profile

i.	Name	Prof. I. J. Shaikh		
ii.	Date of Birth	06-01-1983		
iii.	Unique ID			
iv.	Education Qualification	B.E. (Electronics & Telecommunication Engineering)	M.E. (Electronics & Telecommunicati on Engineering)	
v.	Work Experience	14 Years 6 Months		
	 Teaching 	12 Years		
	• Research	-		
	• Industry	02 Years 6 Months		
	• Others			
vi.	Area of Specialization	Cloud Computing (AWS, A	zure) DevOps	
vii.	Course taught at Diploma/UG/PG	UG • Big Data Analytics, Data Analytics, Internet of Things (IoT), Cloud Computing, Digital Technique, Image Processing, Electronics & Electrical Technology, Industrial Electronics, Microprocessor, Mobile Communication Switching Theory & logic Design,	PG:	
viii.	Research Guidance	UG: 20 Groups (65 Students)	PG:	
ix.	Projects Carried out	20		
X.	Patents			
xi.	Technology Transfer	AWS Certified Solution Architect -Professional		
xii.	Research Publication	National-02	International-04	
xiii.	No. of Books Published with details			

i.	Name	Ms. K. A. Deshmane		
ii.	Date of Birth	25-07-1984		
iii.	Unique ID	1-11304611438		
iv.	Education Qualification	B.E.(Computer Science and Engineering) M.Tech. (Computer Science and Engineering)		
v.	Work Experience	16 Years		
	• Teaching	16 Years		
	• Research	00 Years		
	• Industry	00 Years		
	• Others			
vi.	Area of Specialization	Data Structure, Micropr	ocessor	
vii.	Course taught at Diploma/UG/PG	UG: Data Structure, C Programming, Microprocessor, Adv. Microprocessor, Management Information System	PG: Advanced Distributed System	
viii.	Research Guidance	UG: 30 Groups (120 Students)	PG: 04 Students	
ix.	Projects Carried out	30	04	
х.	Patents		-	
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-04	
xiii.	No. of Books Published with details			

Other

i.	Name	Mrs. M. M. Kulkarni		
ii.	Date of Birth	03-08-1981		5.6
iii.	Unique ID			
iv.	Education Qualification	M. A.(Political Science)	M. Lib & I. Sc.	
v.	Work Experience	12 Years		
	• Teaching			
	• Research			
	• Industry			
	• Others	12 Years		
vi.	Area of Specialization	Library & Information S	Science	
vii.	Course taught at Diploma/UG/PG			
viii.	Research Guidance			
ix.	Projects Carried out	01		
х.	Patents			
xi.	Technology Transfer			
xii.	Research Publication	National-01	International-o	
xiii.	No. of Books Published with details			

9. Fee

FEES STRUCTURE

FOR FIRST YEAR (F.E.) ENGINEERING – 2024-25

Sr. No.	Particulars	OPEN	OBC/ EBC/ SEBC/EWS	VJNT/SBC/ST/TFWS	SC/ST
1	Tuition Fee (Adhoc fee)	72137	-	-	-
2	Development Fee (Adhoc fee)	7863	7863	7863	-
	Total Rs. (1+2)	80000	7863	7863	-
3	Registration & College Admission Form Fee	500	500	5000	500
4	Caution Money Deposit	1000	1000	1000	1000
5	Amartya Shiksha Yojana Policy (Govt. of Maha.)	701	701	701	701
	Total Rs. (3 to 5)	2201	2201	2201	2201
	Total Fees (1 to 5)	82201	10064	10064	2201

^{*} In addition to above fees student has to pay Dr. BATU, Lonere University fees.

FOR DIRECT SECOND YEAR (D.S.E.) ENGINEERING – 2024-25

Sr. No.	Particulars	OPEN	OBC/ EBC/ SEBC/EWS	VJNT/SBC/ST/TFWS	sc
1	Tuition Fee (Adhoc fee)	72137	-	-	-
2	Development Fee (Adhoc fee)	7863	7863	7863	-
	Total Rs. (1+2)	80000	7863	7863	-
3	Registration & College Admission Form Fee	500	500	500	500
4	Caution Money Deposit	1000	1000	1000	1000
5	Amartya Shiksha Yojana Policy (Govt. of Maha.)	454	454	454	454
	Total Rs. (3 to 5)	1954	1954	1954	1954
	Total Fees (1 to 5)	81954	9817	9817	1954

^{*} In addition to above fees student has to pay Dr. BATU, Lonere University fees.

HOSTEL FEES STRUCTURE

FOR FIRST YEAR (F.Y.) ENGINEERING - 2023-24

Sr. No.	Particulars	Girls Hostel Fees (Rs.)
1	Hostel Charges (Per annum)	15000/-

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Admission Position -2023-24

SSWCOE, Solapur

Final F. Y. B. Tech. - 2023-24 Intake & Admitted Students Status

Sr.		Student Intake			Admitted Students (CAP - I, II & CAP- III)					Overall			
No.	Branch S. I.	т	I.L.	EWS	TFWS	Total Intake	S. I.	ACAP	I.L.	EWS	TFWS	Total Admitted	%
1	CSE	96	24	12	6	138	90	2	7	12	5	116	84.06%
2	CSE- AI&DS	72	18	9	5	104	61	3	2	8	5	79	75.96%
3	E&TC	60	0	6	0	66	49	0	0	6	0	55	83.33%
4	Electrical	30	0	3	0	33	0	0	0	1	0	1	3.03%
5	ECE	60	0	6	3	69	26	1	0	3	3	33	47.83%
	Total	318	42	36	14	410	226	6	9	30	13	284	69.27%

SSWCOE, Solapur

Direct Second Year B. Tech. - 2023-24 Admitted Students Status

Sr.		Vacancy		Admitted Students						
No.	Branch	Regular	EWS	Total Vacancy	Regular	ACAP	Total	EWS	Total	%
1	CSE	19	3	22	18	1	19	3	22	100.00%
2	CSE- AI&DS	15	1	16	15	0	15	1	16	100.00%
3	E&TC	56	2	58	53	3	56	2	58	100.00%
4	Electrical	66	6	72	41	5	46	3	49	68.06%
	Total	156	12	168	127	9	136	9	145	86.3%

Admission Procedure

As per the rules and regulations of State Commission Cell and Admission Regulating Authority, Govt. of Maharashtra.

12 Criteria and Weightage for admission

https://sswcoe.edu.in/eligibilty_process.html

13,14 List of Applicants, Results of Admission Under Management Seats

First Year ACAP Advertisement: https://sswcoe.edu.in/adbvertisment.pdf

First Year ACAP Schedule: https://sswcoe.edu.in/IL%20and%20ACAP%20Schedule.pdf

First Year ACAP Merit List:

DSE ACAP Advertisement: https://sswcoe.edu.in/adbvertisment.pdf

DSE ACAP Schedule: https://sswcoe.edu.in/DSE%20%20ACAP%20Schedule.pdf

DSE ACAP Merit List:

15 (A) Information of Infrastructure and Other Resources Available

Sr. No.	Particular	Available	As per AICTE Norms	Actual Area in Sqm.		
1.	Class Room-Tutorial Room (UG)	16+04	14+04 (1056 Sq.m)	1350		
2.	Laboratories	04+30	04+30 1980	2550		
3.	Drawing Halls	01	01 (132 Sq.m)	162		
4.	Computer Centers	01	01 (150 Sq.m)	162		
5.	Central Examination Facility	01	30	82.28		
6.	Barrier Free Built Environment	Provision of Ramp and other necessary requirements made available.				
7.	Occupancy Certificate	Yes, Available				
8.	Fire and Safety Certificate	Yes, Available	2			
9.	Hostel Facilities Girls- Own building in campus.					

11

15 (B)	Library
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S.N.	Particulars	Availability
1	Total area of the library	528.33 Sq.m.
2	Library Basement	313.30 Sq.m.
3	Reading Room	247.96 Sq.m.
4	Seating capacity of the library	300
5	Library Software	'AutoLib NG' Software
6	Reprographic facility	Yes
7	Working hours of library	10:00am to 06:00pm
8	Total Computers	10
9	Total Titles (Purchased)	954
10	Total Volumes	13149
11	Donated Books	1052
12	Total Journals	32
13	E-Books & E-Journals	DELNET Membership
14	NPTEL Videos	All Videos are available in the
		library
15	Library networking facility	Yes
16	Usage data of the library (in terms of books issued to the	65%
	faculty & students etc.)	
17	Annual library budget (% of annual student fee collected)	3%

A.Y. 2024-25

Sr. No.	Particular (Books/ Titles/ Journals)	No. of Titles	No. of Volumes	
1	Computer Science & Engineering	215	4617	
2	Electronics & Telecommunication Engineering	206	2667	
3	Electrical Engineering	143	1293	
4	CSE (AI&DS)	50	1719	
5	Electronics & Computer Engineering	12	550	
6	Civil, Mechanical & IT	184	1471	
7	General Science & Engineering (F.Y.B.Tech)	122	2926	
8	General Books	139	561	
	Total =	1071	15804	
10	National and International Journals	42		
11	E- Library facilities Yes			

Donated Books

Sr. No.	Particular	No. of Titles	No. of Volumes
	Books Title	S	1
1	Civil Engineering	98	111
2	Computer Science & Engineering	57	68
3	Electronics & Telecommunication Engineering	350	652
4	Electrical Engineering	43	59
5	General Books	29	29
6	Engineering Mathematics	11	11
7	Engineering Mechanics	41	41
	Total	629	971

A.Y.2023-24
Details of Library Books

S.N.	Course	Total Number of Titles	Total Number of Volumes	Number of Journals		
				International	National	
1	Computer Science & Engineering	199	3724		06	
2	Electronics & Telecommunication Engineering	175	2178		06	
3	Electrical Engineering	117	1082		06	
4	Information Technology	37	882		06	
5	CSE (AI&DS)	30	684		06	
6	Civil Engineering	122	337		01	
7	Mechanical Engineering	25	76			
8	General Science & Engineering (F.Y.B.Tech)	80	2216		01	
9	General Books	123	561			
10	Technical, Soft Skill	10	407			
11	Engineering Mathematics	36	802			
	Total =	954	13149		32	
	Donated Books	1040	1052			
	Total of Purchased + Donated	1994	14201			

15 (C)	Laboratory and Workshop
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Sr.	Name of	Laboratory Name	Name of Major Equipment/ Facility
No.	Department		
1.	General Science &	1. Physics	Hall Effect Set up,
	Engineering		GM Counter,
			He-Ne laser Source
		2. Chemistry	Hot Oven,
			PH Metry,
			Conductometry,
			Digital Balance
		3. Applied Mechanics	Polygon law of force Apparatus,
			Centroid of irregular shaped bodies
			Apparatus,
			Simply supported reaction Apparatus,
			Inclined plane Apparatus,
			Collision of elastic bodies Apparatus,
			Simple pendulum Apparatus
		4. Drawing Hall	-
		5. Language Lab	21 Computers(Dell Optiplex-3060)
			with Headset & ETNL- Language Lab

	Software
6. Workshop	Resistance Welding Machine,
	Column Drilling Machine,
	Power Hacksaw Machine,
	Press Machine for Sheet Metal,
	Power Grinder,
	Lathe Machine,
	Carpentry Tools,
	Arc Welding Machine,
	Oxy-Fuel Gas Welding /Cutting

i)Computer Programming Lab	20 Computers(Dell Optiplex-3060(i3-Win 10)), EPSON L4160 Printer with Scanner	
ii)Computer Center	20 Computers(Dell Optiplex-3060), Epson Projector	
iii)Advanced Software Lab	20 Computers (Dell OptiPlex- 3070 SFF), EPSON M200 Printer with Scanner	
iv)CSE Project Lab	20 Computers (Dell OptiPlex- 3070 SFF), EPSON M200 Printer with Scanner	
v)Operating System Lab	20 Computers (Dell OptiPlex- 3070 MT XCTO(i5- Linux)), EPSON M200 Printer with Scanner	
vi)Cryptography Lab	20 Computers (Dell OptiPlex- 3070 MT XCTO(i5- Linux)), EPSON M200 Printer with Scanner	
vii)Network and Computing Lab	20 Computers (Dell OptiPlex- 3070 MT XCTO(i5- Linux)), EPSON M200 Printer with Scanner	
viii)CS/ Language Lab	20 Computers (Dell OptiPlex- 3070 MT XCTO(i5- Linux)), EPSON M200 Printer with Scanner	
Electrical Department		
i) Network Analysis Lab	50 MHz DSO Function Generator (3 MHz) DC Dual Power Supply (30V)	
	RLC series Resonance	
	RLC Parallel Resonance	
	Transient Analysis of RC and RL Circuit	
	Transient Analysis of RLC Circuit	
	Constant K - Low Pass Filter	
	Constant K - High Pass Filter	
	Constant K - Band Pass Filter	
	Constant K - Band Stop Filter	

ii) Electrical Machine 1 Motor directly coupled to 3 KW/220V/1500RPM DC Shunt Generator 5HP/220V/1500RPM DC Shunt Motor with Mechanical Loading arrangement 5HP/3.7KW/220V/1500RPM DC Shunt Generator 5 SHP/220V/1500RPM DC Shunt Generator 5 SHP/220V/1500RPM DC Series Motor with Mechanical Loading arrangement Three Point Starter Four Point Starter 1-PhTransformer 3-Ph transformer Bridges, Ammeter, Voltmeter, Wattmeter, LVDT, Rheostats, Single Phase Auto transformer Bridges, Ammeter, Voltmeter, Wattmeter, LVDT, Rheostats, Single Phase Auto transformer 50 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), EASY DRILLING MACHINE: 59 Specification: Drill Size: 0.6 - 6 mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. Digital Electronics and Processors Lab v)Digital Electronic and Processors Lab Specification: Drill Size: 0.6 - 6 mm. 20 Juital Electronic Specification: Drill Size: 0.6 - 6 mm. 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7-segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, Wein Bridge Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Clipper, Clamper using OPAMP, Study of PLL Electronics and Telecommunications		5HP/220V/1500RPM DC Shunt	Ammeter,
Generator SHP/220V/1500RPM DC Shunt Motor with Mechanical Loading arrangement SHP/3.7KW/220V/1500RPM DC Shunt Generator 5 SHP/220V/1500RPM DC Series Motor with Mechanical Loading arrangement There Point Starter Four Point Starter I-phTransformer 3-Ph transformer Bridges, Ammeter, Voltmeter, Wattmeter, LVDT, Rhoestats, Single Phase Auto transformer Bridges, Ammeter, Voltmeter, Wattmeter, LVDT, Rhoestats, Single Phase Auto transformer 50 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), EASY DRILLING MACHINE: Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, RC Phase Shift Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL	ii) Electrical Machine 1	Motor directly coupled to	
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Motor with Mechanical Loading arrangement Three Point Starter Four Point Starter 1-PhTransformer 3-Ph transformer Bridges, Ammeter, Voltmeter, Wattmeter, LVDT, Rheostats, Single Phase Auto transformer 50 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), EASY DRILLING MACHINE: Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. v)Digital Electronics and Processors Lab Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7-segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, RC Phase Shift Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
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iii) Electrical Measurement Lab Wattmeter, LVDT, Rheostats, Single Phase Auto transformer 50 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), EASY DRILLING MACHINE: - Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. v)Digital Electronics and Processors Lab Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7-segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		transformer	
Phase Auto transformer 50 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), EASY DRILLING MACHINE: - Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7-segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
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iv) Electrical Project Lab MHz), DC Dual Power Supply (30V), EASY DRILLING MACHINE: - Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL	in) Electrical Measurement Eas	Phase Auto transformer	
iv) Electrical Project Lab EASY DRILLING MACHINE: - Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
iv) Electrical Project Lab Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm. Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		11 2 \ //	
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height setting: up to 110 mm. Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		1	
v)Digital Electronics and Processors Lab Digital IC trainer, 4 bit digital Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
v)Digital Electronics and Processors Lab Comparator, Ripple Counter 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		height setting: up to 110 mm.	
Processors Lab 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		Digital IC trainer, 4 bit digital	
Processors Lab 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL	y)Digital Electronics and	Comparator, Ripple Counter	
Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL	, ,	3 bit MOD N Counter, Sequence	
Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7- segment Decoder Driver 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		· · ·	
Encoder, BCD adder, BCD to 7- segment Decoder Driver 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
vi) Analog Electronic 50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		segment Decoder Driver	
Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL	vi) Analog Electronic		
& Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		` /	
OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL		•	
Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL			
Clipper , Clamper using OPAMP, Study of PLL		<u> </u>	
Study of PLL		Oscillator using OPAMP,	
		Clipper, Clamper using OPAMP,	
Electronics and Telecommunications		Study of PLL	
	Electronics and Telecommunic	ations	I

		lectronic Devices and ircuits Lab (EDC)	50 MHz DSO, 100 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), Voltage Series & Shunt Feedback Amplifier Kit, OPAMP trainer Kit, Colpitts Oscillator using FET, Wein Bridge Oscillator using FET, RC Phase Shift Oscillator using OPAMP, Clipper, Clamper using OPAMP, Study of PLL	
	ii) N	Network Analysis Lab	50 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), RLC series Resonance, RLC Parallel Resonance, Transient Analysis of RC and RL Circuit, Transient Analysis of RLC Circuit, Constant K - Low Pass Filter, Constant K - High Pass Filter, Constant K - Band Pass Filter, Constant K - Band Stop Filter	
		i) Digital Electronics and Processors Lab	Digital IC trainer, 4 bit digital Comparator, Ripple Counter, 3 bit MOD N Counter, Sequence Generator, Design of FSM, Shift Register Using IC 7495, Priority Encoder, BCD adder, BCD to 7-segment Decoder Driver	
	iv	Electronic Project Lab	50 MHz DSO, Function Generator (3 MHz), DC Dual Power Supply (30V), EASY DRILLING MACHINE: - Specification: Drill Size: 0.6 - 6mm. Spindale Speed: up to 12000 r.p.m. Max. Drilling Depth: 40mm. Range of height setting: up to 110 mm.	
11	,	Workshop	Resistance Welding Machine, Column Drilling Machine, Power Hacksaw Machine, Press Machine for Sheet Metal, Power Grinder, Lathe Machine, Carpentry Tools, Arc Welding Machine, Oxy-Fuel Gas Welding /Cutting	
5-	5-D Computing Facilities			

Sr.No.	Particulars	Available
1.	Internet Bandwidth	100 mbps
2.	Number and configuration of system	187

3.	Total number of system connected by LAN	187
4.	Total number of system connected by WAN	187
5.	Major software packages available	Yes
6.	Special purpose facilities available	Computer Center
		2) ETNL Language Lab Software
		3) Wi-Fi connectivity in Campus
7.	Innovation Cell	Yes
8.	Social Media Cell	Yes
9.	Compliance of the National Academic	-
	Depository (NAD)	

15-E	List of facilities available
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Sr. No.	Particulars	Available
1.	Games and Sports Facilities	Yes
2.	Extra-Curricular Actives	Yes
3.	Soft Skill Development Facilities	Yes

15-F	Teaching Learning Process
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Sr. No.	Particulars	Available
1.	Curricula and syllabus for each of the programs as approved by the University	Yes
2.	Academic Calendar of the University	Yes
3.	Academic Times Table with the name of the faculty members handling the course	Yes
4.	Teaching Load of each faculty	Yes
5.	Internal Continuous Evaluation System and place	Yes
6.	Student's assessment of faculty, system in place	Yes

SSWCOE, SOLAPUR

TOTAL COLLEGE STUDENT STRENGTH - A. Y. – 2023-24

Sr.	Branch	S.I.	F. Y. B. Tech.	S. Y. B. Tech.		T. Y.	Final Yr.	Total		
No.				Regular	DSE	Total	B. Tech.	B. Tech.	Student Strength	Remarks
1	CSE	360	116	125	22	153	147	75	491	-
2	CSE- AI&DS	120	79	58	16	74	77	0	230	-
3	E&TC	240	55	16	58	74	71	37	237	-
4	Electrical	240	1	2	49	51	20	13	85	-
5	I.T.	60	0	0	0	0	0	71	71	
6	ECE	60	33	О	0	0	0	0	33	
	Total	1080	284	201	145	352	315	196	1147	

Placement details of last 3 years

Year	CSE	CSE(AI&DS)	E & TC	Electrical	IT	ECE	Civil
2023-24	63		31	21	36		
2022-23	37		15	08			1
2021-22							

Average Package of placed students is 4 LPA

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Number of Research Projects/ Consultancy Works

(i)List of Research Projects/Consultancy Works

Name of the Project/ Endowments, Chairs	Name of the Principal Investigator	Department of Principal Investigator	Year of Award	Funds provided	Duration of the project
ML approach to detecting human diseases based on behavioral parameters	Dr. T. A. Chavan, Dr. D. B. Lokhande	Computer Science & Engineering	2023	3,00,000/-	2 Years

(ii) List of Research Projects (if any) during UG Projects

Sr. No	Name of Student	Title of Paper	Journal Name	Month & Year of Publication
1	Balmani Balaji Battin, Shrutika Devidas Kadgi, Anjali Govardhan Guda, Lavanya Nandkishor Kadads	Your path for Placement preparation Website	International Journal of Scientific Research in Engineering & Managemnet(IJSRE M)	December-23
2	NIkita Madhukar Gurrum, Aishwarya Ambadas Annam, Ashwini Shrinivas Vangari, Uma Vijay Yerjal	Educon (Educational Connection)	Journal of Emerging Technologies & Innovation Research(JETIR)	December-23
3	Manchikatala Pravalika Mallesham, Annam Rajeshwari Narendra, Yelgeti Akanksha Ambadas, Dhanashri Rajkumar Asade	Faculty Assistant	Journal of Emerging Technologies & Innovation Research(JETIR)	November-23
4	Vitkar Rani Suresh, Mirajkar Tejaswini Prabhat,	Website-Ishwar Foundation Scholarship Website	Journal of Emerging Technologies & Innovation	December-23

	Annam Ushashri Prakash, Uttur Yogita Shankar		Research(JETIR)	
5	Sabiha Munaf Harkare, Kausar kalyani, Susmita Birajdar, Anuja Mallinath Phatate	Inventory Maintenance & Billing System with GST	International Journal of Research & Analytical Reviews(IJRAR)	December-23
6	Sushmita patil, Sanjana Garje, Shraddha Gaikwad, Kanal Gundeli	College (Online) Admission Form System	International Journal of Research & Analytical Reviews(IJRAR)	December- 23
7	Gautami Sarwade, Sonali Sarvagod, Srushti Santosh Totad, Anagha Ande	Blood Bank Application	International Journal of Research & Analytical Reviews(IJRAR)	December-23
8	Sushama Awale, Rutika Pujari, Anjali Late, Laxmi Kaljate	Vegetable Plant Disease Detection& Fertilizer Recommender System	International Journal of Research & Analytical Reviews(IJRAR)	December-23
9	Preeti Harale, Smurti Jogdankar, Vaishnavi Joshi, Priyanka Manchikatala	Solapur City Website:Navigation Solapurs Digital Sphere	International Journal of Research & Analytical Reviews(IJRAR)	December-23
10	Akshata Birajadar, Vaishnavi Mali, Pranjal Solanke, Bhagyshri Dhavane	Stay Here! (Hostel App)	International Journal of Research & Analytical Reviews(IJRAR)	December-23
11	Jyoti Mugale, Bhagyashri Magi, Priya Pinamkar, Manisha Bhosale	Machine Learning Approach to Detect Heart Related Disease	International Journal of Research & Analytical Reviews(IJRAR)	November-23
12	Manisha Giri, Pooja Ajay Miskin, Priti Amol Matthe, Rajeshwari Suresh Ligade	Traffic Detetctor using Blue Light	International Journal of Research & Analytical Reviews(IJRAR)	December-23

I. ENHANCING STUDENTS' ACADEMIC PERFORMANCE

- ✓ Emphasis on student centered learning
- ✓ Additional guidance for analytical/tough subjects
- ✓ Book bank scheme through which students are given 5 to 6 books for the whole semester
- ✓ Use of active learning techniques in classrooms
- ✓ ETLP-(Engineering Teaching Learning Pedagogy)a unique pedagogy which emphasis on students learning
- ✓ Students feedback-Feedbacks regarding academics twice in a semester
- ✓ Library sessions in the time table
- ✓ Two class tests, mid semester exam & preliminary exams are conducted
- ✓ Open book test for CA-2
- ✓ Adherence to academic calendar

II. ENHANCING STUDENTS' EMPLOYABILITY

- ✓ Soft Skill As a Subject In Time Table
- ✓ Industry Oriented Co-Curricular Crash Courses.
- ✓ Focused Industrial Training
- ✓ One month industrial training
- ✓ Motivated to complete skill enhancing courses like NPTEL

III. INSTITUTES' SOCIAL RESPONSIBILITY

- ✓ Institute's Faculty Delivers Guest Lecturers In Polytechnics, Junior and Engineering Colleges
- ✓ Each Department Conducts Workshops For The Faculty Members And Students of Polytechnics.
- ✓ Institute conducts MOCK-CET exams for HSC students

IV. FACULTY WELFARE & MOTIVATION

- ✓ Faculties will be sponsored to pursue Ph. D in NIT(s) / IIT(s) & institutes of excellence.
- ✓ Faculties will be given sponsorship for research publication.
- ✓ Faculty to faculty knowledge & experience sharing activity.
- ✓ Faculty development program.
- ✓ Faculties will be sponsored for NPTEL online course and exam.
- ✓ Employee's Provident Fund(EPF) and Employee Group Insurance benefits are extended to staff

V. MENTOR- MENTEE ACTIVITY

- ✓ Teachers are appointed as mentors per 15 to 20 students
- ✓ A mentor hour is included in time table
- ✓ Mentors motivate their mentees and resolves their academic and personal problems
- ✓ Small skills are inculcated through mentor hours